



GRU 6010

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

By: Manfred Beck

Date: July 21, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Michael Jacob et al.

Applic. No. : 10/053,970

Filed : January 22, 2002

Title : Test Circuit for an Analog Measurement of Bit Line Signals of Ferroelectric Memory Cells

Examiner : Viet Q. Nguyen

Group Art Unit : 2818

RECEIVED
JUL 30 2003
TECHNICAL DIVISION OF BIT
LINE SIGNALS OF FERROELECTRIC MEMORY CELLS

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir :

Responsive to the Office action dated May 8, 2003 kindly amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.